



Japan Regional Standards Committee (JRSC)

Liaison Report

Nahoko Koga / SEMI Japan September, 2025

STANDARDS

CONNECT - COLLABORATE. - INNOVATE. - GROW. - PROSPER

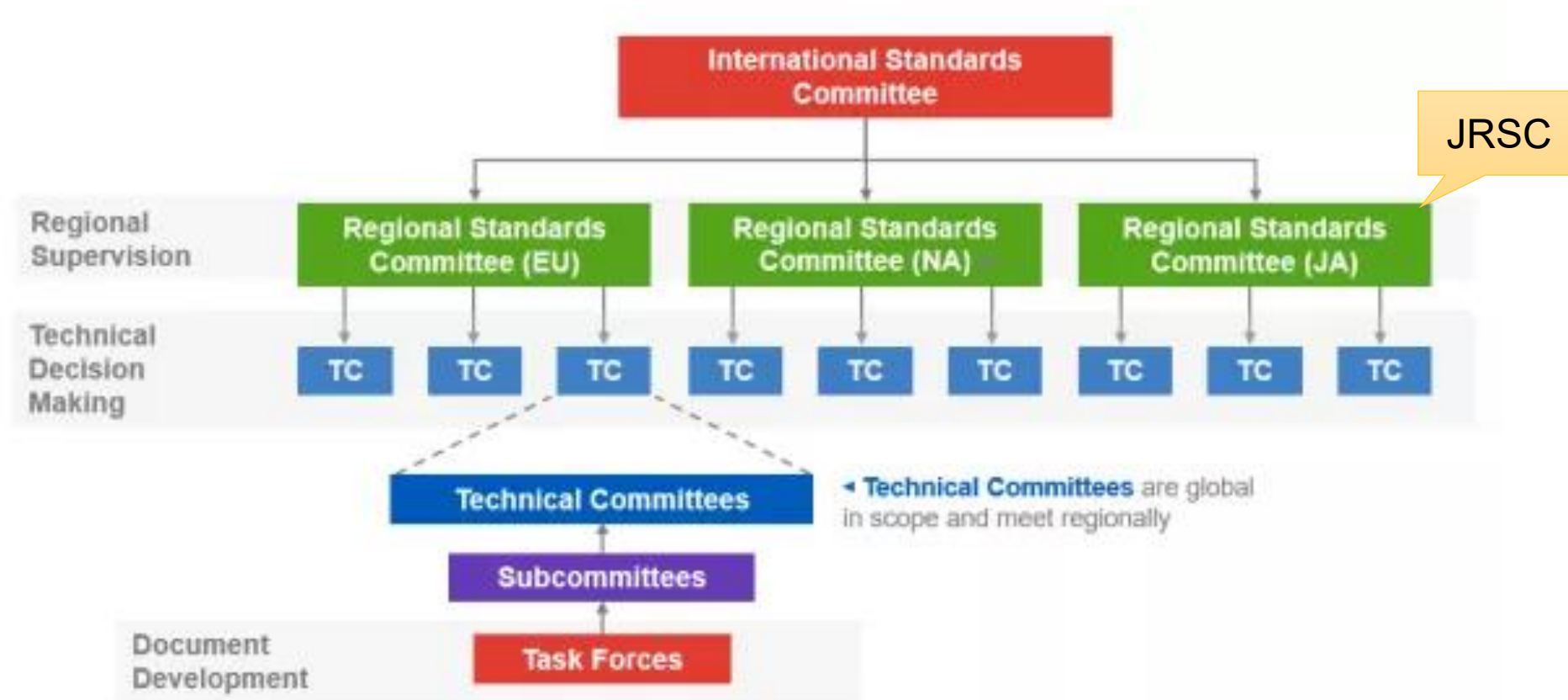
AGENDA

Subtitle

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SEMI Standards Organization

Committee Structure



* China, Korea, and Taiwan Standards activities supported by North America Region

SEMI Japan RSC Organization

**Japan Chapter of
3D Packaging & Integration
Global Technical Committee**
C: Masahiro Tsuruya – INEMI
C: Haruo Shimamoto – AIST
C: Kazunori Kato – AiT

**Japan Chapter of
Automation Technology
Global Technical Committee**
C: Terry Asakawa – Vistaideal Consulting
C: Fumiyasu Obuchi – SIEMENS
C: Yasuhiro Kaneko – YAMAHA Motor

**Japan Chapter of
Compound Semiconductor Materials
Global Technical Committee**
C: Masayoshi Obara – Shin-Etsu Handotai

**Japan Chapter of
Environmental Health & Safety
Global Technical Committee**
C: Supika Mashiro – Tokyo Electron Limited
C: Akiko Tsukada – NuFlare Technology
C: Moray Crawford – Hatsuta Seisakusho

**Japan Chapter of Facilities
Global Technical Committee**
C: Hiromichi Enami – i ZOOM
C: Isao Suzuki – Consultant
C: Masafumi Kitano – Fujikin

**Japan Chapter of
Flexible Hybrid Electronics
Global Technical Committee**
C: Satoshi Maeda – Hiroshima City University
C: Ryoichi Watanabe – Japan Display
C: Tadahiro Furukawa – Yamagata University

**Japan Chapter of
FPD Materials & Components
Global Technical Committee**
C: Tadahiro Furukawa – Yamagata University
C: Ryoichi Watanabe – Japan Display
C: Toshimasa Eguchi – Sumitomo Bakelite

**Japan Chapter of FPD Metrology
Global Technical Committee**
C: Ryoichi Watanabe – Japan Display
C: Akira Kawaguchi – Otsuka Electronics

**Japan Chapter of Gases
Global Technical Committee**
C: Hiromichi Enami – Consultant
C: Isao Suzuki – Consultant
C: Masafumi Kitano – Fujikin

**Japan Chapter of
Information & Control
Global Technical Committee**
C: Takayuki Nishimura – SCREEN
Semiconductor Solutions
C: Mitsuhiro Matsuda – KOKUSAI ELECTRIC
C: Yuko Toyoshima – Hitachi High-Tech

**Japan Chapter of Liquid Chemicals
Global Technical Committee**
C: Yoshiyuki Fujitani – SCREEN
Semiconductor Solutions
C: Takuya Nagafuchi – Nihon Entegris

**Japan Chapter of Metrics
Global Technical Committee**
C: Mitsune Sakamoto – ZAMA Consulting

**Japan Chapter of
Physical Interfaces & Carriers
Global Technical Committee**
C: Tsuyoshi Nagashima – Miraial
C: Daisuke Sado – Eagle Industry
C: Yasuhisa Ito – Murata Machinery

**Japan Chapter of Silicon Wafer
Global Technical Committee**
C: Tetsuya Nakai – SUMCO
C: Ryuji Takeda – GlobalWafers Japan

**Japan Chapter of Traceability
Global Technical Committee**
C: Yoichi Iga – Self
C: Hirokazu Tsunobuchi – GENETEC
Corporation

Special Groups
Reporting to the JRSC

**Standardization Process
Improvement (SPI)**
L: Supika Mashiro – Tokyo
Electron Limited

JRSC Meeting Information

Subtitle

- Last meeting
 - August 28, 2025 11:00-13:30
 - TODA Hall & Conference
- Next meeting
 - December 15, 2025 14:00 - 17:00
 - SEMI Japan, Tokyo, Japan / Official Virtual TC Chapter Meeting (Hybrid)

JRSC Leadership

- Co-chairs
 - Ryuji Takeda – GlobalWafers Japan
 - Daisuke Sado – Eagle Industry
- Vice-chair
 - Supika Mashiro – Tokyo Electron Limited

Authorized Activities

- RSC Membership Update & Approvals
 - Takayuki Nishimura (SCREEN Semiconductor Solutions) was approved to continue as MAL of ISC
 - Kazunori Kato (Advanced Interface Technology) was approved to continue as Advisor of ISC

SEMI Standards Awards Selection Committee 2025

- Chair: Hidetoshi Sakura (Self)
- Members: Takashi Nakagawa (Yokogawa Solution Service), Ryoichi Watanabe (Japan Display), Naoyuki J. Kawai (JSNM), Kazunori Kato (AiT)
- Schedule
 - Aug. 8 SEMI Standards Award Selection Committee was formed.
 - Aug. 28 **The above was reported at the JRSC meeting.**
 - Late Sept. to Early Oct. Nomination period for award candidates by all JRSC members
 - Mid-Oct. Selection and approval of award candidates by the Selection Committee Recommendation will be made to the JRSC.
 - Late Oct. Award recipients will be decided by the JRSC.
 - Dec.18 Award Ceremony at the Friendship Party during SEMICON Japan 2025

SEMI Japan Standards Awards Overview

Awards Name	Reasons for Recognition	Organization to Select/ Approve
SEMI Japan Standards Award	<ul style="list-style-type: none"> Improvement of standards processes Enhancement and dissemination of standards in the industry Broad regional impact 	Based on recommendation by the Standards Award Selection Committee, the JRSC approves
SEMI Japan International Collaboration Award	<ul style="list-style-type: none"> Overcoming language/cultural barriers International coordination Promoting SEMI Standards globally 	
SEMI Japan Honor Award	<ul style="list-style-type: none"> Establishment/reorganization of committees Long-term service (approx. 4+ years) 	
SEMI Japan Special Award	Significant contributions in special fields, equivalent to Standards/International Collaboration Awards	
Technical Committee Award	<ul style="list-style-type: none"> Leadership in document development- Significant contributions as lead developer Committee management Task force coordination- Standards dissemination 	Selected by Technical Committee Chairs, reported to JRSC Chairs

Planning Meeting Updates

Planning Meeting 2025

- The meeting was held on August 28, 2025, 2:00PM – 5:30PM, at TODA Hall & Conference.
- Theme: Gaps in Supply Chain Transparency and SEMI Standards
- Program
 - Opening
 - Introduction: Supply Chain Management Initiative under SEMI
 - Examples:
 - Contained substances – Supika Mashiro (Tokyo Electron)
 - Cyber security – Takayuki Nishimura (SCREEN Semiconductor Solutions)
 - Brainstorming about gaps in supply chain transparency
 - Group Work
 - Presentation based on the group work
 - Closing



THANK YOU

STANDARDS